

(A): Amendments to the specification:

Amend the second paragraph on page 1 to read as follows:

Related Applications:

This invention is related to an application entitled: Multiprocessor computer system having multiple mode changes and a changing mode cache state (which uses an additional cache state to eliminate the need to purge caches when changing the mode bits), USSN 10/603,251, filed June 25, 2003.